

DNP

FID1

DNP

FID2

DNP

FID3

PCB Number: DRV8848EVM

PCB Rev: REV A

PCB

LOGO

Texas Instruments

U1 may get hot

SH-J1

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ5

Assembly Note

Place the shunt across J6

Label Table

Variant	Label Text
001	ChangeMe!
002	ChangeMe!

Designed for:Public Release

Mod. Date: 8/7/2014

Project Title: DRV8848EVM

Sheet Title:

Number: DRV8848EVM| Rev: A

SVN Rev: Not in version control

Drawn By:

Engineer: Anda zhang

Assembly Variant: 001

File: Hardware_ANSI-B.SchDoc

Size: B

Contact: http://www.ti.com/support

Sheet: 1 of 1

http://www.ti.com

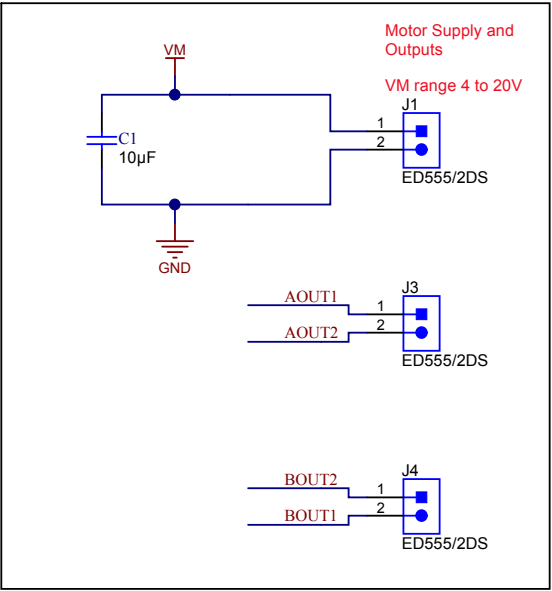
?Texas Instruments_2014

TEXAS

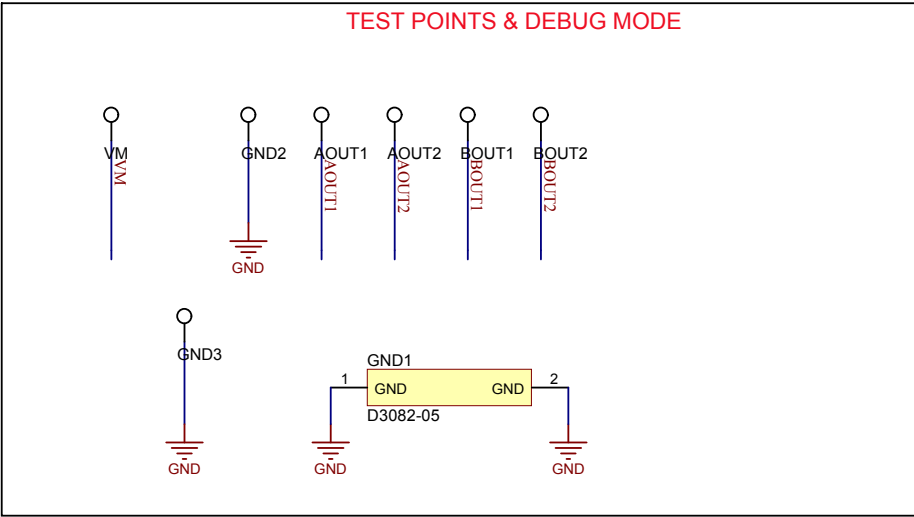
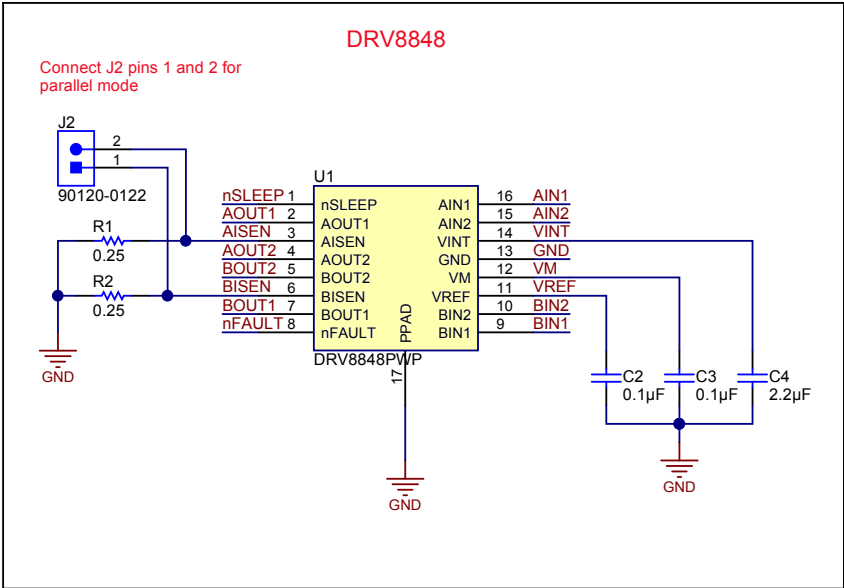
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A

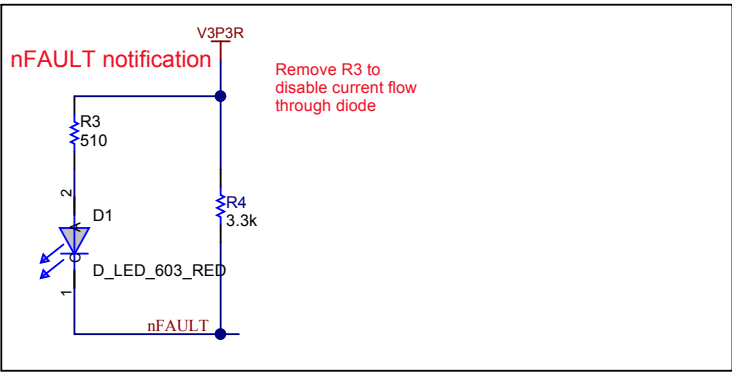


B

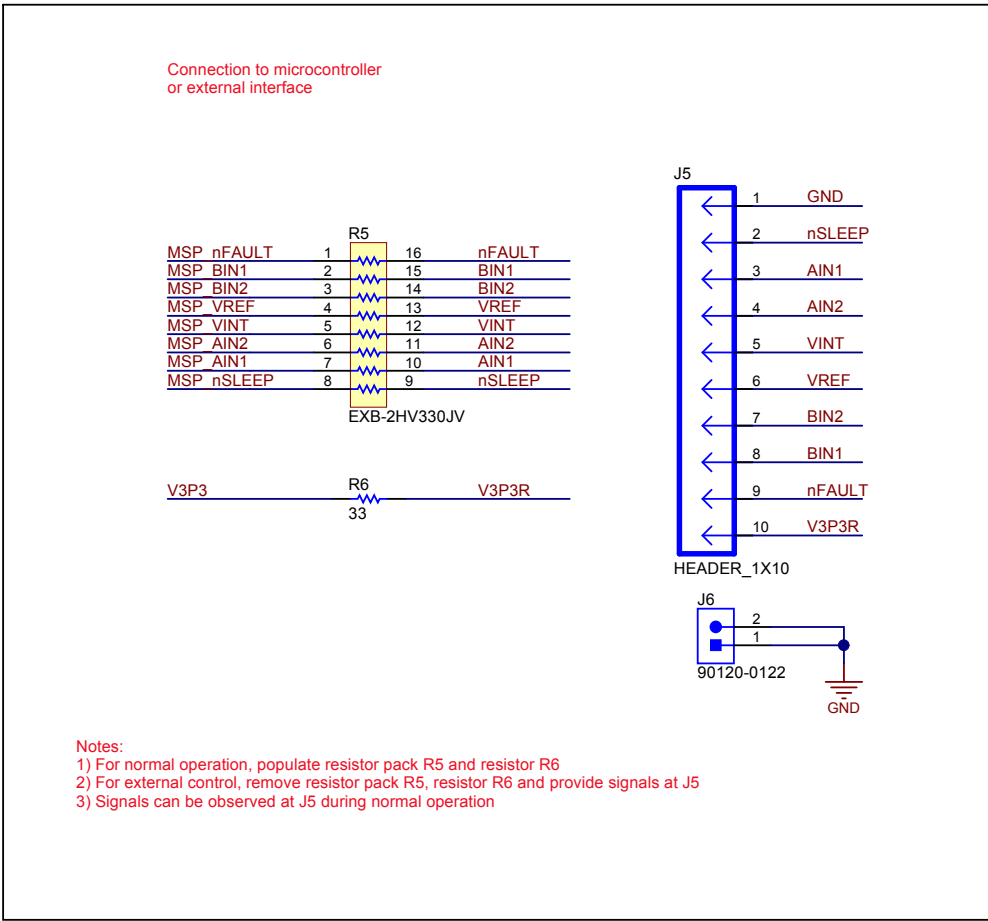


A

C



D



B

C

D

	1	2	3	4	5	6						
A						<table><tr><th colspan="2">Revision History</th></tr><tr><th>Revision</th><th>Notes</th></tr><tr><td></td><td></td></tr></table>	Revision History		Revision	Notes		
Revision History												
Revision	Notes											
B												
C												
D												
	1	2	3	4	5	6						

Number: [DRV8848EVM](#) | Rev: [A](#)

SVN Rev: Not in version control

Drawn By:

Engineer: [Anda zhang](#)

Designed for: [Public Release](#) | Mod. Date: 9/11/2014

Project Title: [DRV8848EVM](#)

Sheet Title:

Assembly Variant: [001](#) | Sheet: [1](#) of [1](#)

File: [Cover Sheet ANSI-B.SchDoc](#) | Size: B

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Designed for: Public Release		Mod. Date: 9/11/2014	
Project Title: DRV8848EVM			
Sheet Title:			
SVN Rev: Not in version control		Assembly Variant: 001	
Drawn By:		File: Cover Sheet_ANSI-B.SchDoc	
Engineer: Anda zhang		Size: B	
Contact: http://www.ti.com/support		http://www.ti.com	
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